AMENDMENTS TO THE DRAWINGS

The attached replacement sheets of drawings include changes to FIGS. 4 and 6. The replacement sheets replace the original sheets including FIGS. 4 and 6, respectively. The replacement sheets include changes to add descriptive text labels to the elements depicted therein.

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (currently amended) An apparatus comprising:

a first device to receive a first signal from an integrated circuit representing a first supply voltage value, ~~associated with~~ a first supply current value associated with the first supply voltage value, ~~and representing~~ a second supply voltage value, and ~~associated with~~ a second supply current value associated with the second supply voltage value, and

to output an output supply voltage to the integrated circuit based on the first signal.

2. (original) An apparatus according to Claim 1, wherein the first signal represents an impedance value.

3. (currently amended) An apparatus according to Claim 1, the first device to adjust ~~a~~ the output supply voltage to a value based at least in part on the first signal.

4. (currently amended) An apparatus according to Claim 3, the first device comprising:

a voltage regulator converter to generate the output supply voltage; and

a voltage regulator controller to receive the first signal and to transmit a control signal to the voltage regulator converter, the control signal to control the value of the output supply voltage.

5. (cancelled)

6. (cancelled)

7. (currently amended) An apparatus according to Claim 3, wherein the output supply voltage is associated with an output supply current, wherein the first supply voltage value and the first supply current value define a first coordinate of a voltage vs. current coordinate system, wherein the second supply voltage value and the second supply current value define a second coordinate of the voltage vs. current coordinate system, wherein the first coordinate and the second coordinate define a line, wherein the value of the output supply voltage and a value of the output supply current define a third coordinate, and wherein the line substantially comprises the third coordinate.

8. (original) An apparatus according to Claim 1, wherein the first signal represents a slope of a power supply load line.

9. (currently amended) An apparatus comprising:

~~a first device~~an integrated circuit to transmit a first signal to a voltage regulator, the first signal representing a first supply voltage value, ~~associated with~~ a first supply current value associated with the first supply voltage value, ~~and representing~~ a second supply voltage value ~~associated with~~, and a second supply current value associated with the second supply voltage value, and

to receive an input supply voltage having a value based on the first signal from the voltage regulator.

10. (original) An apparatus according to Claim 9, wherein the first signal represents an impedance value.

11. (original) An apparatus according to Claim 9, wherein the first signal represents a slope of a power supply load line.

12. (cancelled)

13. (currently amended) An apparatus according to Claim 12, wherein the input supply voltage is associated with an input supply current, wherein the first supply voltage value and the first supply current value define a first coordinate of a voltage vs. current coordinate system, wherein the second supply voltage value and the second supply current value define a second coordinate of the voltage vs. current coordinate system, wherein the first coordinate and the second coordinate define a line, wherein the value of the input supply voltage and a value of the input supply current define a third coordinate, and wherein the line substantially comprises the third coordinate.

14. (cancelled)

15. (cancelled)

16. (currently amended) A method comprising:

receiving a first signal from an integrated circuit representing a first supply voltage value, ~~associated with~~ a first supply current value associated with the first supply voltage value, ~~and~~

~~representing~~ a second supply voltage value, and ~~associated with~~ a second supply current value associated with the second supply voltage value; and

outputting an output supply voltage to the integrated circuit based on the first signal.

17. (original) A method according to Claim 16, wherein the first signal represents an impedance value.

18. (currently amended) A method according to Claim 16, further comprising:

adjusting ~~a~~ the output supply voltage to a value based at least in part on the first signal.

19. (currently amended) A method according to Claim ~~1~~816, wherein ~~generating~~ outputting the supply voltage signal comprises:

receiving the first signal;

determining ~~the~~ a value of the output supply voltage based at least in part on the first signal; and

transmitting a control signal to control a voltage regulator converter to generate the output supply voltage.

20. (currently amended) A method according to Claim 18, wherein the output supply voltage is associated with an output supply current, wherein the first supply voltage value and the first supply current value define a first coordinate of a voltage vs. current coordinate system, wherein the second supply voltage value and the second supply current value define a second coordinate of the voltage vs. current coordinate system, wherein the first coordinate and the second coordinate define a line, wherein the value of the output supply voltage and a value of the

output supply current define a third coordinate, and wherein the line substantially comprises the third coordinate.

21. (original) A method according to Claim 16, wherein the first signal represents a slope of a power supply load line.

22. (currently amended) A method according to Claim 16, further comprising:

~~adjusting a supply voltage having a value based at least in part on the first signal; and~~

receiving a second signal representing a third supply voltage value associated with the first supply current value, and representing a fourth supply voltage value associated with the second supply current value.

23. (original) A method according to Claim 22, wherein the second signal represents a second impedance value.

24. (original) A method according to Claim 22, wherein the second signal represents a slope of a second power supply load line.

25. (currently amended) A method according to Claim 22, further comprising:

adjusting the output supply voltage to a ~~second~~ value based at least in part on the second signal.

26. (currently amended) A method according to Claim 25, wherein the second output supply voltage is associated with a second output supply current, wherein the third supply voltage value and the first supply current value define a first coordinate of a voltage vs. current coordinate system, wherein the fourth supply voltage value and the second supply current value define a second coordinate of the voltage vs. current coordinate system, wherein the first coordinate and the second coordinate define a line, wherein the value of the second output supply voltage and a value of the second output supply current define a third coordinate, and wherein the line substantially comprises the third coordinate.

27. (currently amended) A method comprising:

transmitting a first signal from an integrated circuit to a voltage regulator, the first signal representing a first supply voltage value, ~~associated with~~ a first supply current value associated with the first supply voltage value, ~~and representing~~ a second supply voltage value, and ~~associated with~~ a second supply current value associated with the second supply voltage value; and

receiving an input supply voltage at the integrated circuit from the voltage regulator, the input supply voltage having a value based on the first signal.

28. (original) A method according to Claim 27, wherein the first signal represents an impedance value.

29. (original) A method according to Claim 27, wherein the first signal represents a slope of a power supply load line.

30. (cancelled)

31. (currently amended) A method according to Claim 30, wherein the input supply voltage is associated with an input supply current, wherein the first supply voltage value and the first supply current value define a first coordinate of a voltage vs. current coordinate system, wherein the second supply voltage value and the second supply current value define a second coordinate of the voltage vs. current coordinate system, wherein the first coordinate and the second coordinate define a line, wherein the value of the input supply voltage and a value of the input supply current define a third coordinate, and wherein the line substantially comprises the third coordinate.

32. (cancelled)

33. (currently amended) A system comprising:

a microprocessor to transmit a first signal representing a first supply voltage value, ~~associated with~~ a first supply current value associated with the first supply voltage value, ~~and representing~~ a second supply voltage value, and ~~associated with~~ a second supply current value associated with the second supply voltage value;

a voltage regulator to receive the first signal and to output an output supply voltage to the microprocessor based on the first signal; and

a double data rate memory electrically coupled to the microprocessor.

34. (original) A system according to Claim 33, wherein the first signal represents an impedance value.

35. (original) A system according to Claim 33, wherein the first signal represents a slope of a power supply load line.

36. (cancelled)

37. (currently amended) A system according to Claim 36, the voltage regulator comprising:

a voltage regulator converter to generate the output supply voltage; and

a voltage regulator controller to receive the first signal and to transmit a control signal to the voltage regulator converter, the control signal to control the value of the output supply voltage.

38. (currently amended) A system according to Claim 36, wherein the output supply voltage is associated with an output supply current, wherein the first supply voltage value and the first supply current value define a first coordinate of a voltage vs. current coordinate system, wherein the second supply voltage value and the second supply current value define a second coordinate of the voltage vs. current coordinate system, wherein the first coordinate and the second coordinate define a line, wherein the value of the output supply voltage and a value of the output supply current define a third coordinate, and wherein the line substantially comprises the third coordinate.